



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Frank Vanselow

Serial No.: 10/044,764

Filed: 01/09/2002

For: Circuit Assembly for Generating a Phase-Locked Frequency-Modulatable Carrier Frequency Signal

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Docket: TID-29699

Examiner: TBD

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TRANSMITTAL OF FORMAL DRAWINGS

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JUL 23 2002

Technology Center 2600

Assistant Commissioner of Patents  
Washington, DC 20231  
Attention: Official Drafts Person

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner For Patents, Washington, DC 20231.

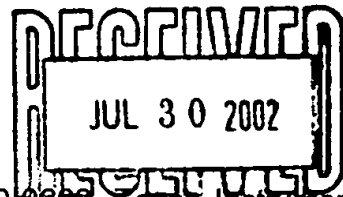
William B. Kempler, Reg. No. 28,228

Date

Dear Sir:

Submitted herewith are 1 sheet of formal drawings.

Charge any necessary fee to Deposit Account No. 20-0668, Texas Instruments Incorporated. This authorization is enclosed in triplicate.



Respectfully submitted,

William B. Kempler  
Senior Corporate Patent Counsel  
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